

## Lecture 2

### Part R

*Case Study on Reactive Systems -  
Bridge Controller  
2nd Refinement: Livelock/Divergence*

# Current m2 May Livelock

**ML\_tl\_green**

when

- ✓  $ml\_tl = \text{red}$
- ✓  $a + b < d$
- ✓  $c = 0$

then

- $ml\_tl := \text{green}$
- $il\_tl := \text{red}$

end

**IL\_tl\_green**

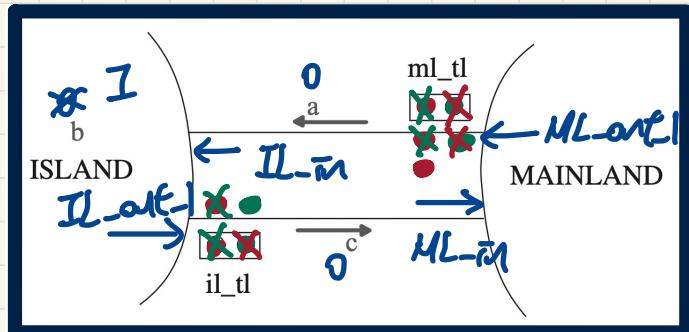
when

- $il\_tl = \text{red}$
- $b > 0$
- $a = 0$

then

- $il\_tl := \text{green}$
- $ml\_tl := \text{red}$

end



Expected trace: no divergent transitions

$d=2$

$\langle \text{init}, \underline{\text{ML\_tl\_green}}, \underline{\text{ML\_out}_1}, \underline{\text{IL\_tl}_1},$

$\downarrow$

a new event

(old events)

$\rightarrow \text{Is } \underline{\text{ML\_tl\_g.}} \text{ enabled? }$

$\rightarrow \text{Is } \underline{\text{IL\_tl\_g.}} \text{ enabled? }$

$\rightarrow \text{Is } \underline{\text{IL\_tl\_g.}} \text{ enabled? }$

$\langle$	<u>init</u>	,	<u>ML_tl_green</u>	,	<u>ML_out_1</u>	,	<u>IL_in</u>	,	<u>IL_tl_green</u>	,	<u>ML_tl_green</u>	,	<u>IL_tl_green</u>	,	$\dots \rangle$
	$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		$d = 2$		
	$a' = 0$		$a' = 0$		$a' = 1$		$a' = 0$		$a' = 0$		$a' = 0$		$a' = 0$		
	$b' = 0$		$b' = 0$		$b' = 0$		$b' = 1$		$b' = 1$		$b' = 1$		$b' = 1$		
	$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		$c' = 0$		
	$ml\_tl = \text{red}$		$ml\_tl' = \text{green}$		$ml\_tl'' = \text{green}$		$ml\_tl' = \text{green}$		$ml\_tl'' = \text{red}$		$ml\_tl' = \text{red}$		$ml\_tl'' = \text{red}$		
	$il\_tl = \text{red}$		$il\_tl' = \text{red}$		$il\_tl'' = \text{red}$		$il\_tl' = \text{red}$		$il\_tl'' = \text{red}$		$il\_tl' = \text{red}$		$il\_tl'' = \text{green}$		

$ml\_tl'' = \text{red}$	$ml\_tl'' = \text{green}$	$ml\_tl'' = \text{red}$
$il\_tl'' = \text{green}$	$il\_tl'' = \text{red}$	$il\_tl'' = \text{green}$



pattern  
of divergence

# Fixing m2: Regulating Traffic Light Changes

To break the divergence patterns  
after both view part  
occurring, some old events clear.

Divergence Trace: <init, ML\_tl\_green, ML\_out\_1, IL\_in, IL\_tl\_green, ML\_tl\_green, ML\_out\_1, IL\_in, ...>

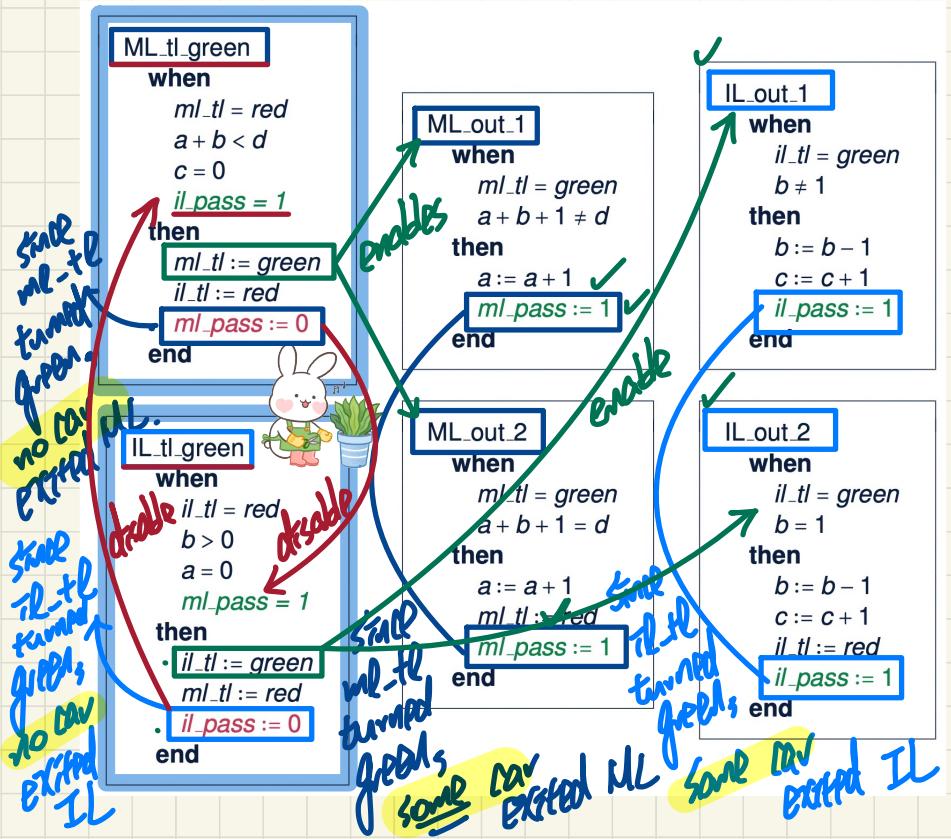


Table showing the state of `ml_pass` and `il_pass` for each event in the trace.

	d = 2	ml_pass	il_pass
< init,		1	1
ML_tl_green,	0	I	I
ML_out_1,	I	I	I
ML_out_2,	I	I	I
IL_in,	I	I	I
IL_in,	I	I	I
IL_tl_green,	I	0	I
IL_out_1,	I	I	I
IL_out_2,	I	I	I
ML_in,	I	I	I
ML_in	I	I	I

Annotations:

- `ml_pass` and `il_pass` are highlighted in blue.
- `0` indicates a false value.
- `I` indicates an initial value.
- `1` indicates a true value.
- A blue arrow points from the `ml_pass` column to the `IL_out_2` row, with the text `→ 'ml_pass'   
 tl-tl both red`.

# Fixing m2: Measuring Traffic Light Changes

```

ML_tl_green
when
  ml_tl = red
  a + b < d
  c = 0
  il_pass = 1
then
  ml_tl := green
  il_tl := red
  ml_pass := 0
end

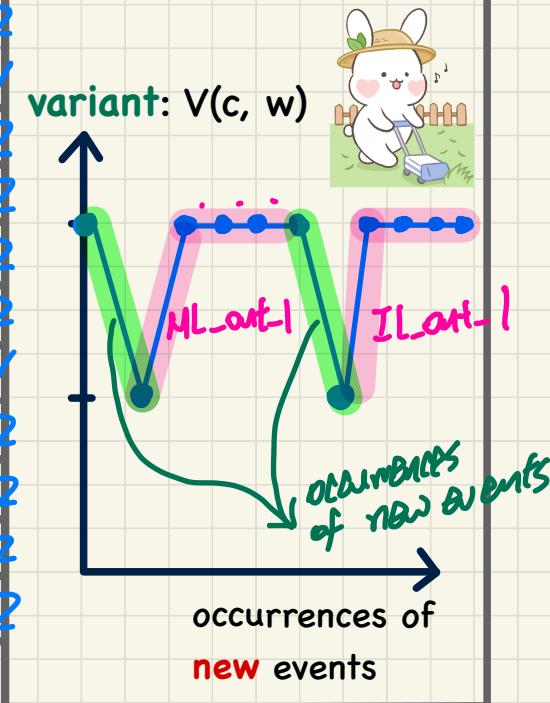
```

```

IL_tl_green
when
  il_tl = red
  b > 0
  a = 0
  ml_pass = 1
then
  il_tl := green
  ml_tl := red
  il_pass := 0
end

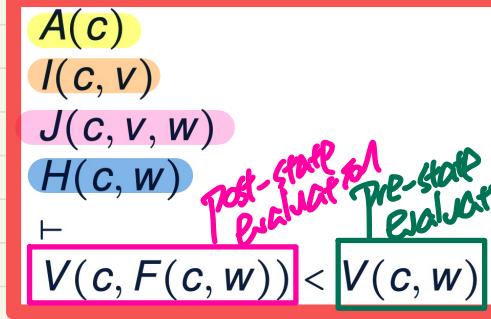
```

$d = 2$	ml_pass	il_pass	variants: $ml\_pass + il\_pass$
< init,	1	1	2
ML_tl_green,	0	1	1
ML_out_1, .	1	1	2
ML_out_2, .	1	1 dd	2
IL_in, .	1	1 Bob	2
IL_in, .	1	1	2
IL_tl_green,	1	0	1
IL_out_1, .	1	1	2
IL_out_2, .	1	1 dd	2
ML_in, .	1	1 BobS	2
ML_in	1	1	2
>			



# PO of Convergence/Non-Divergence/Livelock Freedom

## A New Event Occurrence Decreases Variant



VAR  
 applicable  
 to new  
 events

Variants:  $ml\_pass + il\_pass$

$$* \frac{0}{ml\_pass + il\_pass} < \frac{TL\_pass}{ml\_pass + TL\_pass}$$

ML\_tl\_green/VAR

```

ML_tl_green
when
  ml_tl = red
  a + b < d
  c = 0
  il_pass = 1
then
  ml_tl := green
  il_tl := red
  ml_pass := 0
end
  
```

BIP:  
~~ml\_pass = 0~~  
~~il\_pass = 1~~  
~~tl\_pass = 0~~  
~~tl\_pass = 1~~  
~~tl\_pass = 0~~



$d \in \mathbb{N}$	$d > 0$
$COLOUR = \{green, red\}$	$green \neq red$
$n \in \mathbb{N}$	$n \leq d$
$a \in \mathbb{N}$	$b \in \mathbb{N}$
$a + b + c = n$	$a = 0 \vee c = 0$
$ml\_tl \in COLOUR$	$il\_tl \in COLOUR$
$ml\_tl = green \Rightarrow a + b < d \wedge c = 0$	$il\_tl = green \Rightarrow b > 0 \wedge a = 0$
$ml\_tl = red \vee il\_tl = red$	
$ml\_pass \in \{0, 1\}$	$il\_pass \in \{0, 1\}$
$ml\_tl = red \Rightarrow ml\_pass = 1$	$il\_tl = red \Rightarrow il\_pass = 1$
$ml\_tl = red$	$a + b < d$
$il\_pass = 1$	

$* 0 + il\_pass < ml\_pass + il\_pass$

Concrete guards of  
ML\_tl\_green

## Lecture 2

### Part S

***Case Study on Reactive Systems -  
Bridge Controller  
2nd Refinement:  
Relative Deadlock Freedom***

# PO of Relative Deadlock Freedom

```

axm0.1 { d ∈ ℕ
axm0.2 { d > 0
axm2.1 { COLOUR = {green, red}
axm2.2 { green ≠ red
inv0.1 { n ∈ ℕ
inv0.2 { n ≤ d
inv1.1 { a ∈ ℕ
inv1.2 { b ∈ ℕ
inv1.3 { c ∈ ℕ
inv1.4 { a + b + c = n
inv1.5 { a = 0 ∨ c = 0
inv2.1 { ml_tl ∈ COLOUR
inv2.2 { il_tl ∈ COLOUR
inv2.3 { ml_tl = green ⇒ a + b < d ∧ c = 0
inv2.4 { il_tl = green ⇒ b > 0 ∧ a = 0
inv2.5 { ml_tl = red ∨ il_tl = red
inv2.6 { ml_pass ∈ {0, 1}
inv2.7 { il_pass ∈ {0, 1}
inv2.8 { ml_tl = red ⇒ ml_pass = 1
inv2.9 { il_tl = red ⇒ il_pass = 1
    { a + b < d ∧ c = 0
    { c > 0
    { a > 0
    { b > 0 ∧ a = 0
    { ml_tl = red ∧ a + b < d ∧ c = 0 ∧ il_pass = 1
    { il_tl = red ∧ b > 0 ∧ a = 0 ∧ ml_pass = 1
    { ml_tl = green ∧ a + b + 1 ≠ d
    { ml_tl = green ∧ a + b + 1 = d
    { il_tl = green ∧ b ≠ 1
    { il_tl = green ∧ b = 1
    { a > 0
    { c > 0
  
```

Disjunction of *abstract* guards



Disjunction of *concrete* guards

## Abstract m1

variables: a, b, c

invariants:

- inv1.1 : a ∈ ℕ
- inv1.2 : b ∈ ℕ
- inv1.3 : c ∈ ℕ
- inv1.4 : a + b + c = n
- inv1.5 : a = 0 ∨ c = 0

ML\_out

```

when
  a + b < d
  c = 0
then
  a := a + 1
end
  
```

ML\_in

```

when
  c > 0
then
  c := c - 1
end
  
```

IL\_in

```

when
  a > 0
then
  a := a - 1
  b := b + 1
end
  
```

IL\_out

```

when
  b > 0
  a = 0
then
  b := b - 1
  c := c + 1
end
  
```

## Concrete m2

ML\_tl.green

```

when
  ml_tl = red
  b > 0
  a = 0
  ml_pass = 1
then
  ml_tl := green
  ml_pass := 0
end
  
```

IL\_tl\_green

```

when
  il_tl = green
  a + b + 1 ≠ d
then
  a := a + 1
  ml_pass := 1
end
  
```

ML\_out\_1

```

when
  ml_tl = green
  a + b + 1 ≠ d
then
  a := a + 1
  ml_pass := 1
end
  
```

IL\_out\_1

```

when
  il_tl = green
  b ≠ 1
then
  b := b - 1
  c := c + 1
  il_pass := 1
end
  
```

guards of ML\_out in m<sub>1</sub>  
guards of ML\_in in m<sub>1</sub>  
guards of IL\_in in m<sub>1</sub>  
guards of IL\_out in m<sub>1</sub>

guards of ML\_tl.green in m<sub>2</sub>  
guards of IL\_tl.green in m<sub>2</sub>  
guards of ML\_out\_1 in m<sub>2</sub>  
guards of ML\_out\_2 in m<sub>2</sub>  
guards of IL\_out\_1 in m<sub>2</sub>  
guards of IL\_out\_2 in m<sub>2</sub>  
guards of ML\_in in m<sub>2</sub>  
guards of IL\_in in m<sub>2</sub>

guards of ML\_out in m<sub>1</sub>  
guards of ML\_in in m<sub>1</sub>  
guards of IL\_in in m<sub>1</sub>  
guards of IL\_out in m<sub>1</sub>

ML\_out\_2

when

ml\_tl = green

a + b + 1 = d

then

a := a + 1

ml\_tl := red

ml\_pass := 1

end

IL\_out\_2

when

il\_tl = green

b = 1

then

b := b - 1

c := c + 1

il\_pass := 1

end

IL\_in

```

when
  a > 0
then
  a := a - 1
  b := b + 1
end
  
```

ML\_in

```

when
  c > 0
then
  c := c - 1
end
  
```

# Discharging POs of m2: Relative Deadlock Freedom

```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $\text{COLOUR} = \{\text{green}, \text{red}\}$ 
 $\text{green} \neq \text{red}$ 
 $n \in \mathbb{N}$ 
 $n \leq d$ 
 $a \in \mathbb{N}$ 
 $b \in \mathbb{N}$ 
 $c \in \mathbb{N}$ 
 $a + b + c = n$ 
 $a = 0 \vee c = 0$ 
 $ml\_tl \in \text{COLOUR}$ 
 $il\_tl \in \text{COLOUR}$ 
 $ml\_tl = \text{green} \Rightarrow a + b < d \wedge c = 0$ 
 $il\_tl = \text{green} \Rightarrow b > 0 \wedge a = 0$ 
 $ml\_tl = \text{red} \vee il\_tl = \text{red}$ 
 $ml\_pass \in \{0, 1\}$ 
 $il\_pass \in \{0, 1\}$ 
 $ml\_tl = \text{red} \Rightarrow ml\_pass = 1$ 
 $il\_tl = \text{red} \Rightarrow il\_pass = 1$ 
 $a + b < d \wedge c = 0$ 
 $\vee c > 0$ 
 $\vee a > 0$ 
 $\vee b > 0 \wedge a = 0$ 
 $\vdash$ 
     $ml\_tl = \text{red} \wedge a + b < d \wedge c = 0 \wedge il\_pass = 1$ 
 $\vee il\_tl = \text{red} \wedge b > 0 \wedge a = 0 \wedge ml\_pass = 1$ 
 $\vee ml\_tl = \text{green}$ 
 $\vee il\_tl = \text{green}$ 
 $\vee a > 0$ 
 $\vee c > 0$ 

```



Ex.1

Study

Ex.2

```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $b \in \mathbb{N}$ 
 $ml\_tl = \text{red}$ 
 $il\_tl = \text{red}$ 
 $ml\_tl = \text{red} \Rightarrow ml\_pass = 1$ 
 $il\_tl = \text{red} \Rightarrow il\_pass = 1$ 
 $\vdash$ 
     $b < d \wedge ml\_pass = 1 \wedge il\_pass = 1$ 
 $\vee b > 0 \wedge ml\_pass = 1 \wedge il\_pass = 1$ 

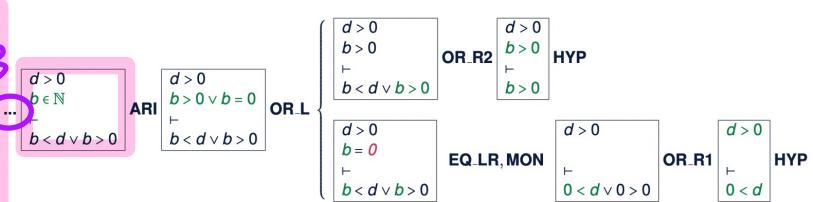
```

Ex.3

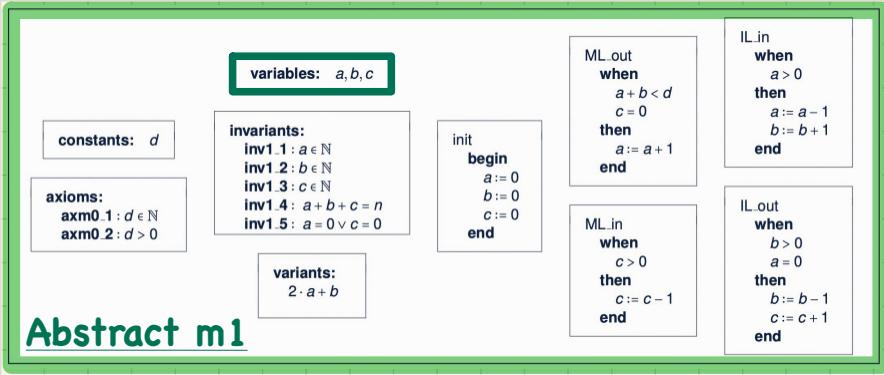
```

 $d \in \mathbb{N}$ 
 $d > 0$ 
 $b \in \mathbb{N}$ 
 $ml\_tl = \text{red}$ 
 $il\_tl = \text{red}$ 
 $ml\_pass = 1$ 
 $il\_pass = 1$ 
 $\vdash$ 
     $b < d \wedge ml\_pass = 1 \wedge il\_pass = 1$ 
 $\vee b > 0 \wedge ml\_pass = 1 \wedge il\_pass = 1$ 

```



# 1st Refinement and 2nd Refinement: Provably Correct



## Correctness Criteria:

- + Guard Strengthening
- + Invariant Establishment
- + Invariant Preservation
- + Convergence
- + Relative Deadlock Freedom

